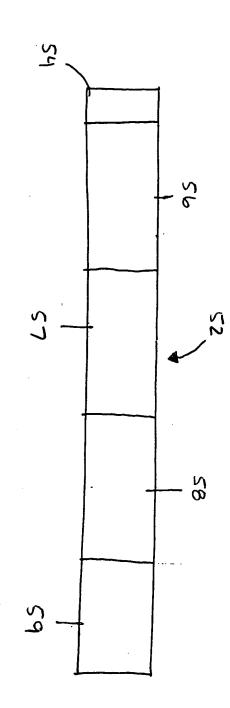


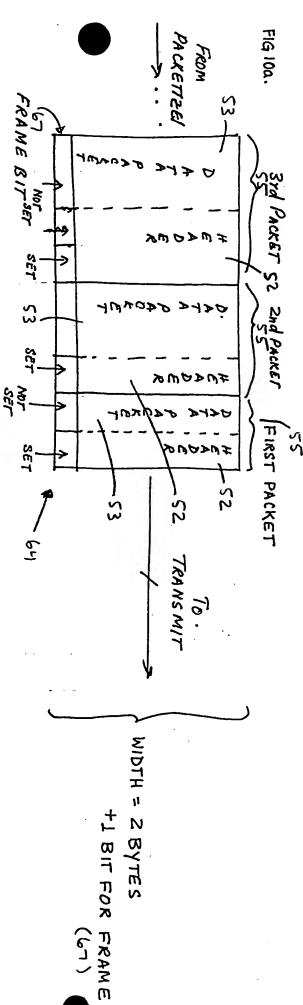
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F19.3

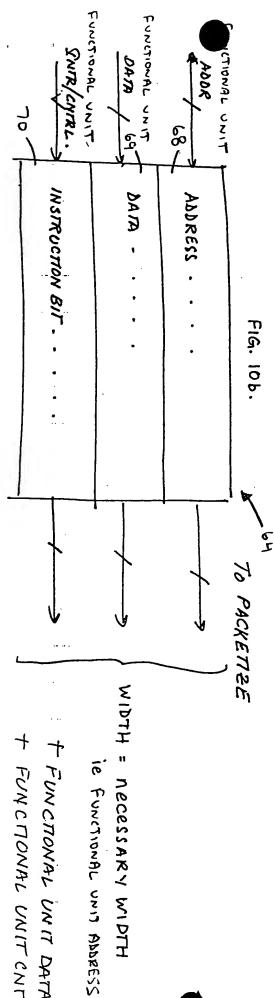


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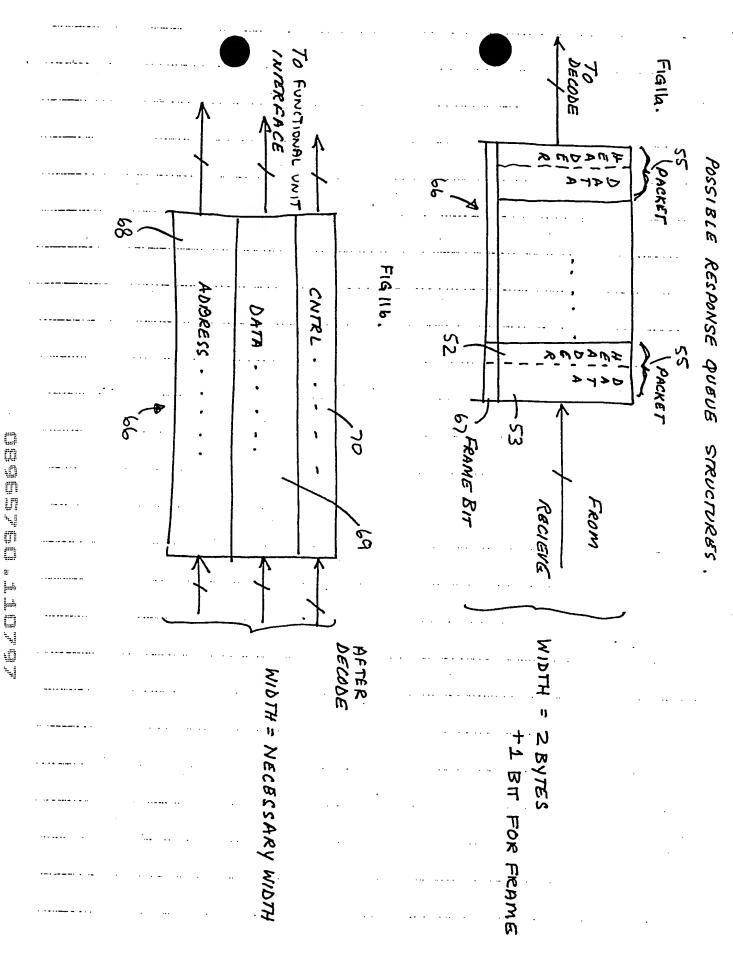


DEPTH = nx maximum PACKET SIZE (258 BYTES) n=1,2,3..

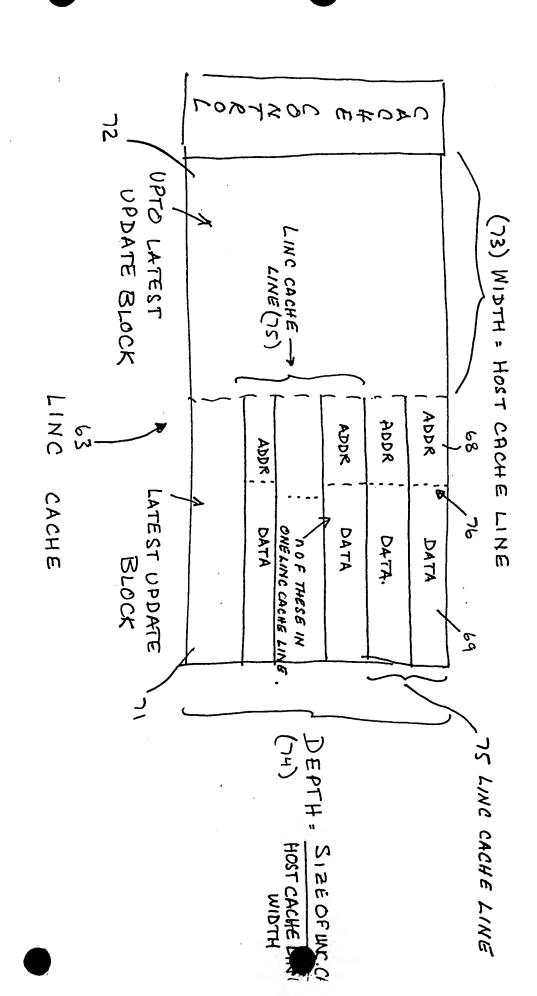


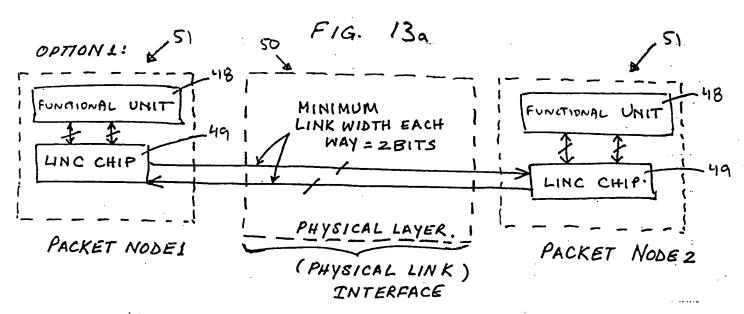
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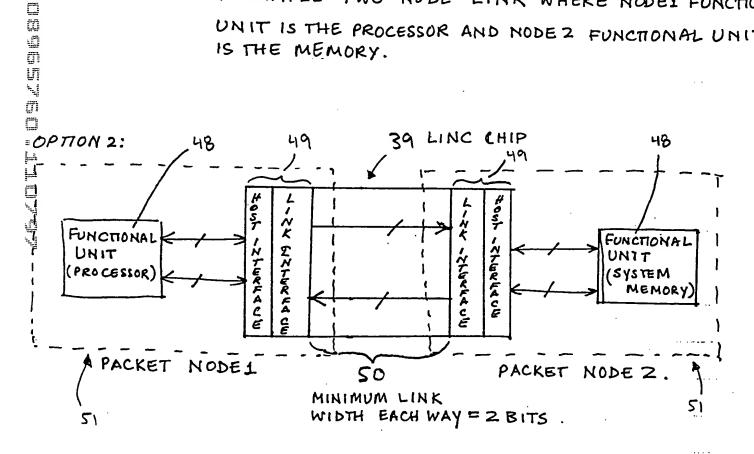


F16.12.



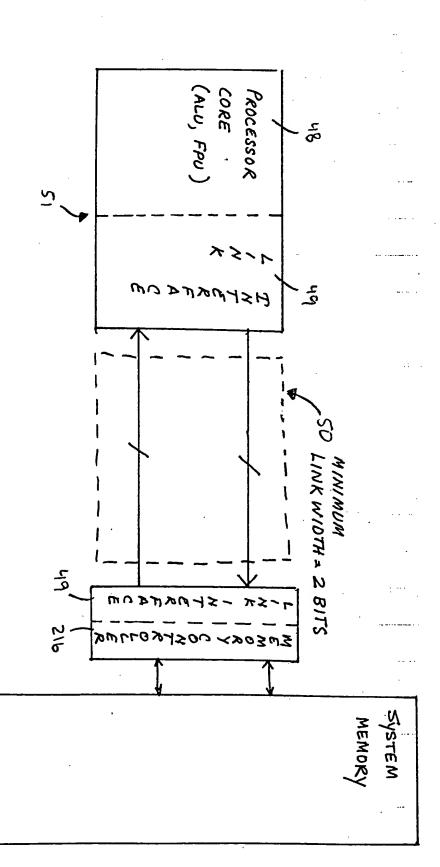


A SAMPLE TWO NODE LINK WHERE NODE! FUNCTIONAL UNIT IS THE PROCESSOR AND NODE 2 FUNCTIONAL UNIT IS THE MEMORY.



F16. 136

78



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FIG. 15

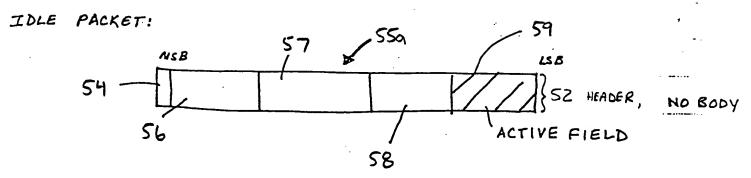
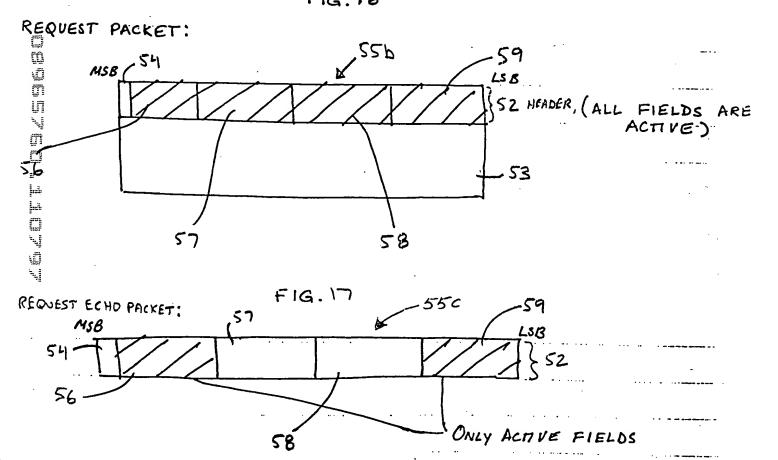


FIG. 16



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FIG. 18

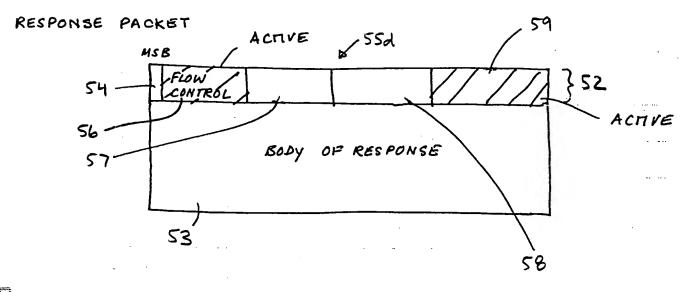
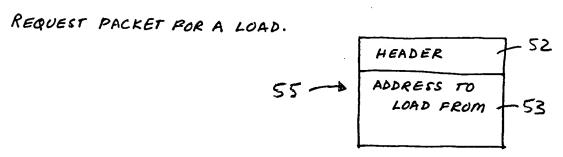
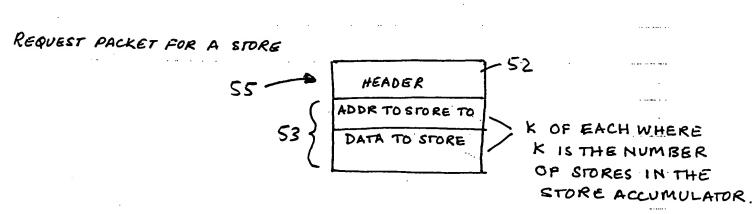
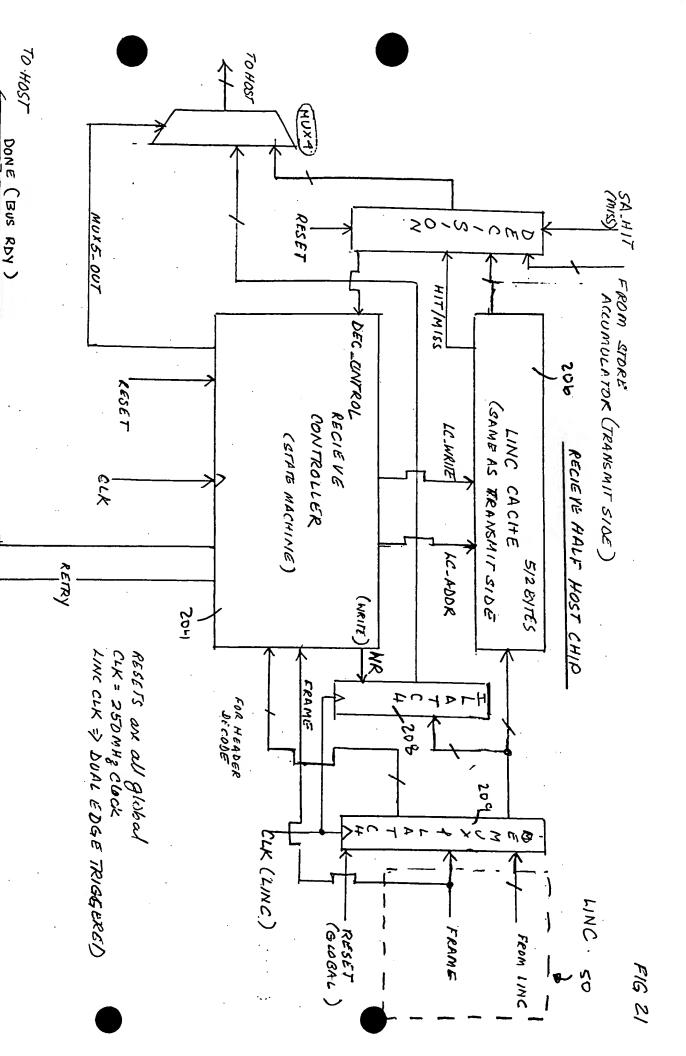


Fig. 19



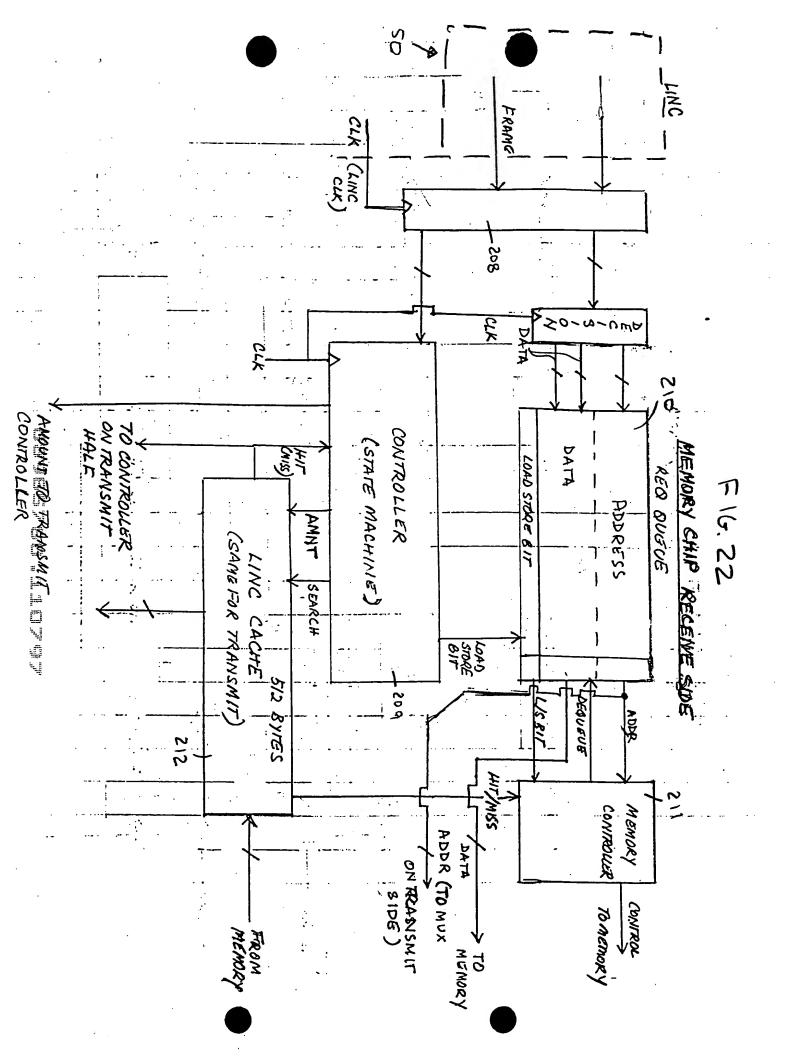


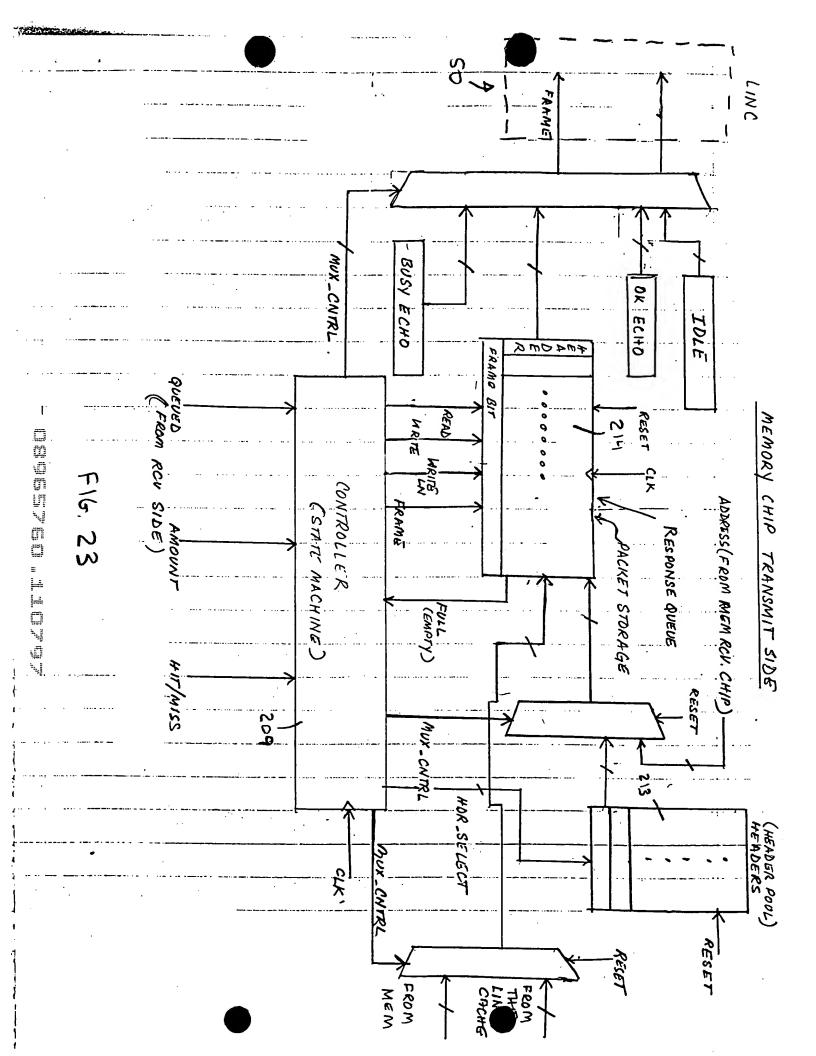
F1G.20



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TO RANSMIT HALF





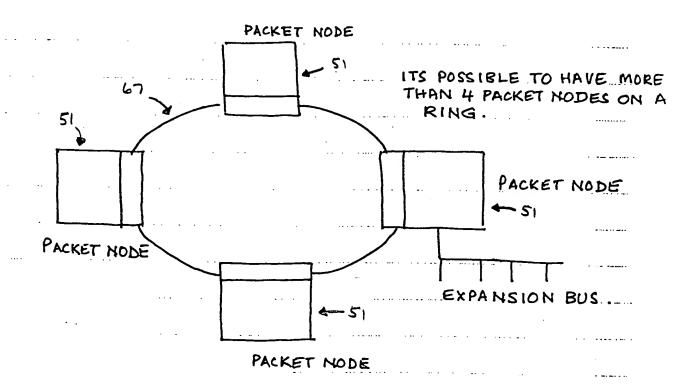


FIG. 25

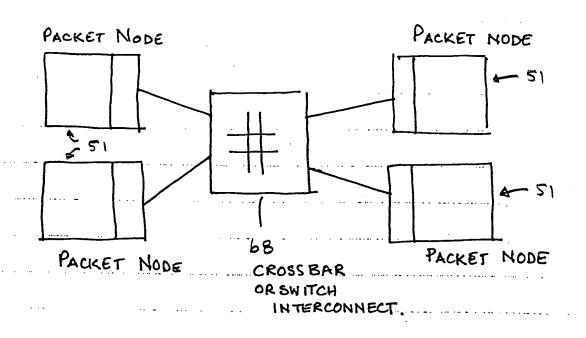


FIG. 26.

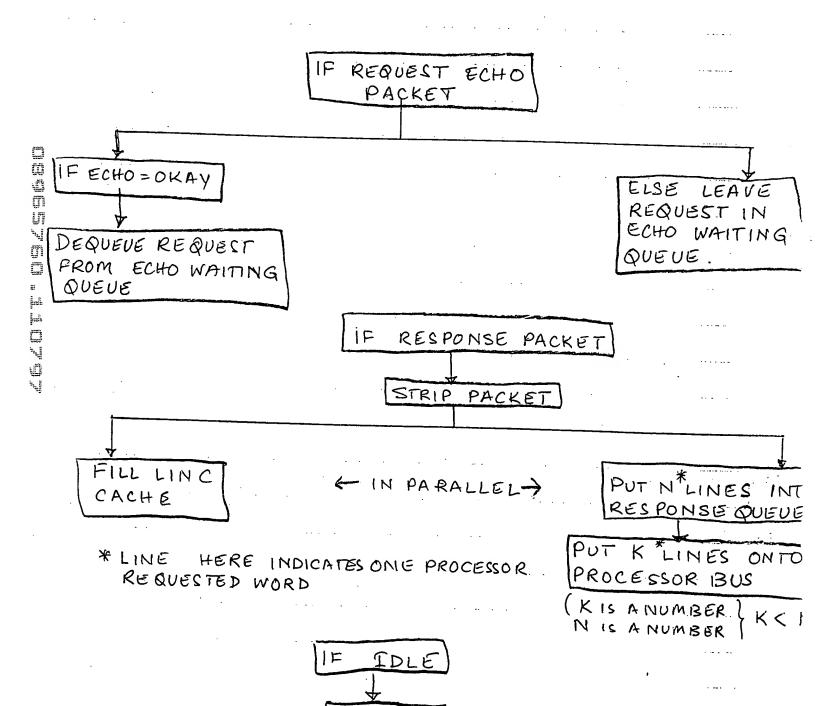
POSSIBLE RESPONSE PACKETS FOR A REQUEST WHEN ACCESS TO TLB AND OR BTB IN THE PROCESSOR IS AVAILABLE

RESPONSE PACKET 55d		REQUEST (MISS) PACKET 556	ERROR IN SPECULATI EXECUTION)
HEADER	{-52-{	HEADER	
DATA FOR ADDRESS n	FOR PC=X	ADDRESS N	LOAD FROM PC (PROGRAM
DATA FOR ADDRESS n + OFFSe	FOR PC= X+1	= SOME NUM	BER X COUNTER)
DATA FOR ADDRESS Y	FOR PC = X+1	AND JUMP	
DATA FOR ADDRESS	1/		
DATA FOR ADDRESS 2	FOR PC = X -	<i>†</i> 2	
DATA NEEDED IN CAS	FOF A JUMP AT	PC=X+2	
DATA NEEDED IN CA			
AND SO ON.		to the state of th	······································
FOR PROGRAMS WITH VERY	HIGH LOCALITY (GRAPHICS , MULI	TMEDIA ETC.)
RESPONSE PA			
52 HEADER			
DATA FOR ADDRESS N+OFFSET DATA FOR ADDRESS		PC = X PC = X+1	· · · · · · · · · · · · · · · · · · ·
1+ 20FFSET DATA FOR ADDRE	_	PC = X+2	
AND:	50 ON 53		
•			

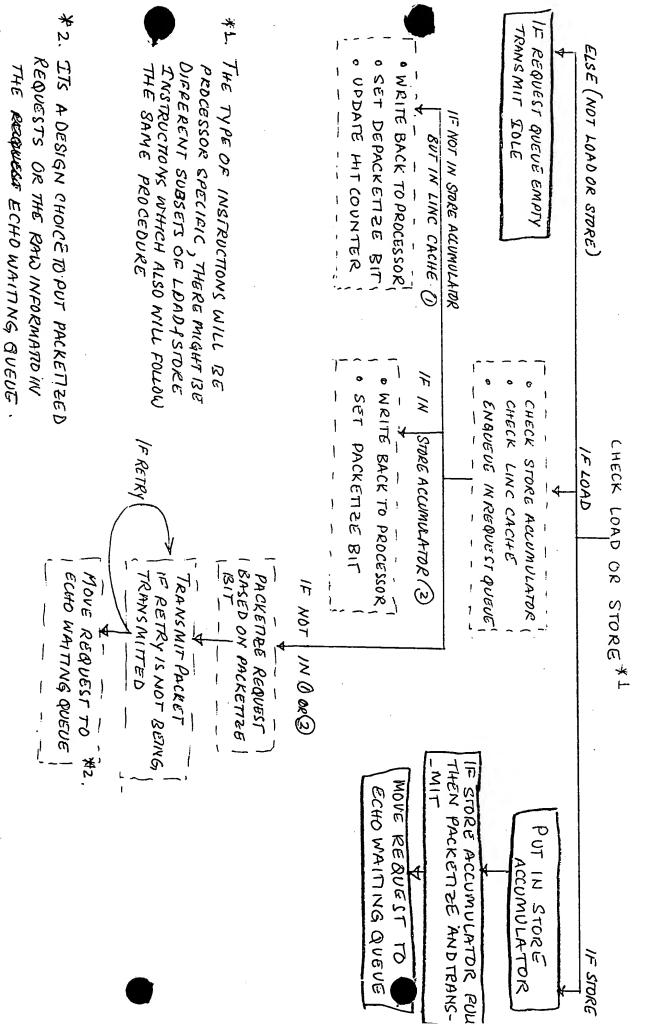
Fig. 27.

PROCESSOR NODE RECEIVE PROTOCOL

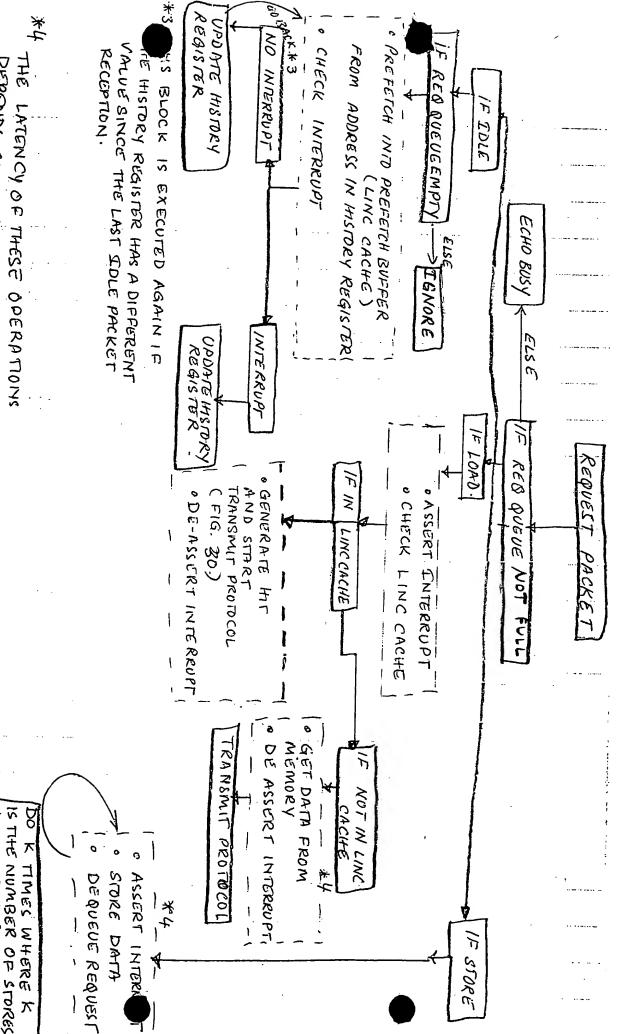
FOR PRESENT DETAILED IMPLEMENTATION THE MEMORY NOI IS EXPECTED TO SEND ONLY THE FOLLOWING PACKETS



HG.



MEMORY NODE RECIEVE PROPOCOL.



. DEPENDS ON THE

: AND THE DEVICES THEMSELVES.

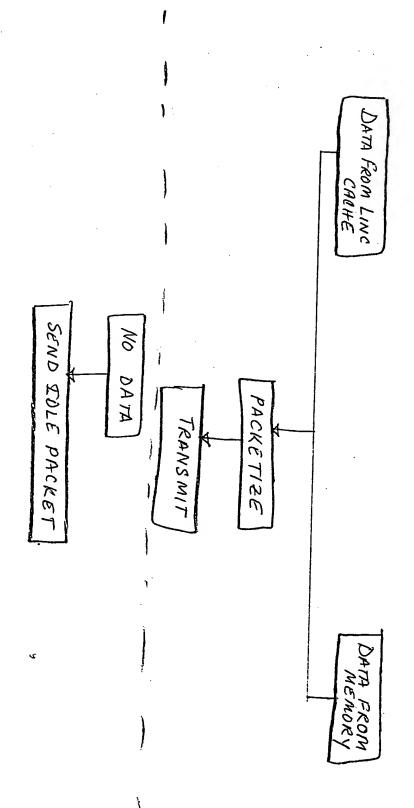
MEMORY ARCHITECTURG

N THE REQUEST PACKET

DE ASSER!

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MEMORY NODE TRANSMIT PROTOCOL.



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